The Department of Electrical and Computer Engineering is seeking a qualified individual to teach ECE 407 Digital Processing II for the Summer 2017 (May to August) academic term. The calendar description of the course can be found below:

**ELEC 407 Digital Processing II**  
Units: 1.5  
Hours: 3-0  
Formerly: ELEC 407

**Digital Signal Processing II**
**Note:** Credit will be granted for only one of ECE 407, ELEC 407, ELEC 458.  
**Prerequisite(s):** ECE 310 or ELEC 310.

**REQUIRED QUALIFICATIONS AND EXPERIENCE**
- The successful individual will have a Ph.D. and relevant industrial experience with the subject matter.  
- Prior teaching experience at a university level is an asset.  
- Salary is commensurate with the qualifications and follows the Sessional Lecturer Salary Grid included in the agreement between the University of Victoria and CUPE Local 4163 (Component 3).  
- Professional engineering registration (PEng) is highly desired.  
- **IF YOU ARE A GRADUATE STUDENT APPLYING FOR THIS POSITION, YOUR APPLICATION MUST INCLUDE A LETTER FROM YOUR SUPERVISOR(S) INDICATING HIS/HER/THEIR AGREEMENT WITH YOUR ACCEPTING THIS POSITION SHOULD IT BE OFFERED TO YOU.**

The availability of this position is subject to funding and enrollment criteria. The University of Victoria reserves the right to fill additional teaching assignments from the pool of applicants for this posting.

The University of Victoria is an equity employer and encourages applications from women, persons with disabilities, visible minorities, and aboriginal persons.

**TO APPLY:** Please submit an expression of interest together with a recent CV via email to eceasst@uvic.ca Attention: Chair, Department of Electrical and Computer Engineering by:  
**16 February 2018.**

The anticipated date by which employment decisions will be made is 23 February 2018.