Term of Appointment from: 1 January 2021 to 30 April 2021.

The Department of Electrical and Computer Engineering is seeking a qualified individual to BME 499 Design Project / ECE 499 / SENG 499 - Design Project II for the Spring 2021 (January to April) academic term. The calendar descriptions of the courses may be found at the links below:

**BME 499 - Design Project**
- Units: 1.5
- Hours: lecture-lab-tutorial 1-9-0
- Academic Calendar Link: [https://www.uvic.ca/calendar/undergrad/index.php#/courses/r1Xq0vTmV?bc=true&bcCurrent=Design%20Project&bcItemType=Courses](https://www.uvic.ca/calendar/undergrad/index.php#/courses/r1Xq0vTmV?bc=true&bcCurrent=Design%20Project&bcItemType=Courses)

**ECE 499 - Design Project II**
- Formerly: CENG 499, ELEC 499
- Units: 1.5
- Hours: lecture-lab-tutorial 1-9-0
- Academic Calendar Link: [https://www.uvic.ca/calendar/undergrad/index.php#/courses/Byg9luamV?bc=true&bcCurrent=Design%20Project%20II&bcItemType=Courses](https://www.uvic.ca/calendar/undergrad/index.php#/courses/Byg9luamV?bc=true&bcCurrent=Design%20Project%20II&bcItemType=Courses)

**SENG 499 - Design Project II**
- Units: 1.5
- Hours: lecture-lab-tutorial 1-9-0
- Academic Calendar Link: [https://www.uvic.ca/calendar/undergrad/index.php#/courses/SkelgYTXN?bc=true&bcCurrent=Design%20Project%20II&bcItemType=Courses](https://www.uvic.ca/calendar/undergrad/index.php#/courses/SkelgYTXN?bc=true&bcCurrent=Design%20Project%20II&bcItemType=Courses)

**REQUIRED QUALIFICATIONS AND EXPERIENCE**

- The successful individual will have a Ph.D. degree, or be enrolled in a Ph.D. degree and must have demonstrated knowledge or experience with the subject matter.
- Prior teaching experience at a university level is an asset.
- Salary is commensurate with the qualifications and follows the Sessional Lecturer Salary Grid included in the agreement between the University of Victoria and CUPE Local 4163 (Component 3).
- **IF YOU ARE A GRADUATE STUDENT APPLYING FOR THIS POSITION, YOUR APPLICATION MUST INCLUDE A LETTER FROM YOUR SUPERVISOR(S) INDICATING HIS/HER/THEIR AGREEMENT WITH YOUR ACCEPTING THIS POSITION SHOULD IT BE OFFERED TO YOU.**

The availability of this position is subject to funding and enrollment criteria. The University of Victoria reserves the right to fill additional teaching assignments from the pool of applicants for this posting.

The University of Victoria is an equity employer and encourages applications from women, persons with disabilities, visible minorities, and aboriginal persons.

**TO APPLY:** Please submit an expression of interest together with a recent CV via email to eceasst@uvic.ca

Attention: Chair, Department of Electrical and Computer Engineering by: **6 November 2020.**

The anticipated date by which employment decisions will be made is 13 November 2020.