The Department of Electrical and Computer Engineering is seeking a qualified individual to teach ECE 403 / ECE 503 Optimization for Machine Learning for the Fall 2019 (September to December) academic term. These two courses are expected to share lectures as well as some assignments and tests. The calendar description of the ECE 403 and ECE 503 courses can be found below:

**ECE 403**  
Optimization for Machine Learning  
Units: 1.5  
Hours: 3-1.5-0  
Formerly: ELEC 403

The steepest descent, Newton, conjugate, and quasi-Newton algorithms for unconstrained optimization. Inexact line search techniques. Application of optimization methods to classification, logistic regression, and support vector machines for signal processing and machine intelligence involving audio, image, video, and other types of data. Introduction to constrained optimization.

*Note:* Credit will be granted for only one of ECE 403, ELEC 403.

**Prerequisite(s):** Minimum fourth-year standing in the Faculty of Engineering.

**ECE 503**  
Optimization for Machine Learning  
Units: 1.5  
Formerly: ELEC 503

The steepest-descent, Newton, conjugate, and quasi-Newton algorithms for unconstrained optimization. Inexact line-search techniques. Application of optimization methods to classification, logistic regression, and support vector machines for signal processing and machine intelligence involving audio, image, video and other types of data. Introduction to constrained optimization. Students are required to complete a project.

*Note:* Credit will be granted for only one of ECE 503, ECE 403, ELEC 403, ELEC 503.

**REQUIRED QUALIFICATIONS AND EXPERIENCE**

- The successful individual will have a Ph.D. and relevant industrial experience with the subject matter.
- Prior teaching experience at a university level is an asset.
- Salary is commensurate with the qualifications and follows the Sessional Lecturer Salary Grid included in the agreement between the University of Victoria and CUPE Local 4163 (Component 3).
- Professional engineering registration (PEng) is highly desired.
- **IF YOU ARE A GRADUATE STUDENT APPLYING FOR THIS POSITION, YOUR APPLICATION MUST INCLUDE A LETTER FROM YOUR SUPERVISOR(S) INDICATING HIS/HER/THEIR AGREEMENT WITH YOUR ACCEPTING THIS POSITION SHOULD IT BE OFFERED TO YOU.**

The availability of this position is subject to funding and enrollment criteria. The University of Victoria reserves the right to fill additional teaching assignments from the pool of applicants for this posting.

The University of Victoria is an equity employer and encourages applications from women, persons with disabilities, visible minorities, and aboriginal persons.

**TO APPLY:** Please submit an expression of interest together with a recent CV via email to eceasst@uvic.ca  
Attention: Chair, Department of Electrical and Computer Engineering by: **28 June 2019.**

The anticipated date by which employment decisions will be made is **5 July 2019.**