
Sessional Lecturer posting – CUPE 4163 (Component 3)

ECE 310 - Digital Signal Processing I

The Department of Electrical and Computer Engineering is seeking a qualified individual for the Sessional Lecturer position (CUPE 4163, Component 3) to teach **ECE 310 - Digital Signal Processing I** for the Summer 2026 (May 1 – August 31, 2026) academic term.

The **ECE 310** course description may be found in the Undergraduate Academic Calendar [here](#).

Salary is commensurate with the qualifications and follows the Sessional Lecturer salary grid included in the Collective Agreement between the [University of Victoria and CUPE 4163 \(Component 3\)](#).

QUALIFICATIONS AND EXPERIENCE:

Preference will be given to applicants with:

- Knowledge or experience with the subject matter
- Prior teaching experience at the university level, and
- A combination of expertise and experience appropriate to the course
- A PhD degree, or currently enrolled in a PhD degree

HOW TO APPLY:

Applications should be addressed to Dr. Michael McGuire, Chair, Department of Electrical and Computer Engineering and emailed to eceasst@uvic.ca by no later than **Tuesday, March 3, 2026 at 10:00am**.

Applications should include:

- A cover letter outlining your qualifications to teach this course
 - Your curriculum vitae
 - Proof of consultation and agreement from your supervisor *****for graduate students*****
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The availability of this position is subject to funding and enrollment criteria. The University of Victoria reserves the right to fill additional teaching assignments from the pool of applicants for this posting.

UVic is committed to upholding the values of equity, diversity, and inclusion in our living, learning and work environments. In pursuit of our values, we seek members who will work respectfully and constructively with differences and across levels of power. We actively encourage applications from members of [groups experiencing barriers to equity](#). Read our full equity statement here, www.uvic.ca/equitystatement.