Sessional Lecturer posting for CUPE 4163 (Component 3)
Department of Electrical and Computer Engineering
Faculty of Engineering and Computer Science

ECE455 Real Time Computer Systems Design Project

Term of Appointment from: 1 January 2024 to 30 April 2024.

The Department of Electrical and Computer Engineering is seeking a qualified individual to teach ECE455 Real Time Computer Systems Design Project for the Spring 2024 (January – April) academic term. The calendar description of the course can be found below.

ECE455 Real Time Computer Systems Design Project  Units 1.5  Hours: lecture-lab-tutorial 3-3-0  Formerly CENG 455

Techniques to guarantee the completion of computations by their deadline. Scheduling for periodic and non-periodic tasks. Organization and functionality of real time system software and hardware. Working in teams, students must complete a project involving substantial real time design and implementation, partly based on the knowledge and skills acquired in earlier coursework. Learning outcomes are based on a progress review, presentation, demonstration, and final report of the prototype design.

Note(s)
- Credit will be granted for only one of ECE 455, CENG 455.

Prerequisites
- Complete 1 of:
  - ECE355 - Microprocessor-Based Systems (1.5)
  - CENG355 - Microprocessor-Based Systems (1.5)
  - CSC355 - Digital Logic and Computer Organization (1.5)

REQUIRED QUALIFICATIONS AND EXPERIENCE
- The successful individual will have a Ph.D. degree, or be enrolled in a Ph.D. degree and must have demonstrated knowledge or experience with the subject matter.
- Prior teaching experience at a university level is an asset.
- Salary is commensurate with the qualifications and follows the Sessional Lecturer Salary Grid included in the agreement between the University of Victoria and CUPE Local 4163 (Component 3).
- IF YOU ARE A GRADUATE STUDENT APPLYING FOR THIS POSITION, YOUR APPLICATION MUST INCLUDE A LETTER FROM YOUR SUPERVISOR(S) INDICATING HIS/HER/THEIR AGREEMENT WITH YOUR ACCEPTING THIS POSITION SHOULD IT BE OFFERED TO YOU.

The availability of this position is subject to funding and enrollment criteria. The University of Victoria reserves the right to fill additional teaching assignments from the pool of applicants for this posting.

The University of Victoria is an equity employer and encourages applications from women, persons with disabilities, visible minorities, and aboriginal persons.

TO APPLY: Please submit an expression of interest together with a recent CV via email to eceasst@uvic.ca Attention: Chair, Department of Electrical and Computer Engineering by: 30 October 2023 at 10:00am.