The Department of Electrical and Computer Engineering is seeking a qualified individual to teach CENG 356 Engineering System Software for the Fall 2017 (September to December) academic term. The calendar description of the course can be found below:

**CENG 356 Engineering System Software**  
Units: 1.5  
System software principles, components, usage, protection, and their relation to hardware and engineering systems. Modern operating systems characteristics and engineering applications; portable operating system interface standard. Requirements, design, development, and maintenance of complex software for portable devices, real-time systems, and multi-processor systems.

**Note:** May not be counted for credit towards a Computer Science degree program.

**Prerequisites:** CENG 255

The anticipated date by which employment decisions will be made is **7 July 2017**.

**REQUIRED QUALIFICATIONS AND EXPERIENCE**

- The successful individual will have a Ph.D. and relevant industrial experience with the subject matter.
- Prior teaching experience at a university level is an asset.
- Salary is commensurate with the qualifications and follows the Sessional Lecturer Salary Grid included in the agreement between the University of Victoria and CUPE Local 4163 (Component 3).
- Professional engineering registration (PEng) is highly desired.
- **IF YOU ARE A GRADUATE STUDENT APPLYING FOR THIS POSITION, YOUR APPLICATION MUST INCLUDE A LETTER FROM YOUR SUPERVISOR(S) INDICATING HIS/HER/THEIR AGREEMENT WITH YOUR ACCEPTING THIS POSITION SHOULD IT BE OFFERED TO YOU.**

The availability of this position is subject to funding and enrollment criteria. The University of Victoria reserves the right to fill additional teaching assignments from the pool of applicants for this posting.

The University of Victoria is an equity employer and encourages applications from women, persons with disabilities, visible minorities, and aboriginal persons.

**TO APPLY:** Please submit an expression of interest together with a recent CV via email to eceasst@uvic.ca Attention: Chair, Department of Electrical and Computer Engineering by: **30 June 2017.**

**REVISED 25 May 17**