ELEC 320 Electronic Devices: I  
Spring 2016 (201601)

Instructor  
Dr. Chris Papadopoulos  
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Office Hours  
Day: Tuesday  
Time: 2:30PM – 5:00PM  
(or by appointment)  
Location: EOW 429

Lectures  
Sections: A01/CRN 21107, A02/CRN 21108  
Days: TWF  
Time: 8:30AM – 9:20AM  
Location: BWC A104

Labs  
B01 M 4:00PM – 6:50PM ELW A309  
B02 M 4:00PM – 6:50PM ELW A309  
B03 R 1:00PM – 3:50PM ELW A309  
B04 R 1:00PM – 3:50PM ELW A309  
B05 T 1:30PM – 4:20PM ELW A309  
B06 T 1:30PM – 4:20PM ELW A309  
B07 T 4:30PM – 7:20PM ELW A309

Website  
http://coursespaces.uvic.ca/ (NetLink ID required)

Required Text  
Modular Series on Solid State Devices, I-IV  
Author: Pierret, Neudeck  
Publisher: Addison-Wesley  
Edition: Second

Reference texts  
Solid State Electronic Devices  
Author: Streetman, Banerjee

An Introduction to Semiconductor Devices  
Author: Neamen

Solid-State Electronic Devices: An Introduction  
Author: Papadopoulos

Topics  
I  Review of Electrical Properties of Materials  
II  Junctions and Diodes  
III  Bipolar Transistors  
IV  Field Effect Transistors

Assessment  
Assignments 10% (Due Jan. 19; Feb. 2; Mar. 1; Mar. 18)  
Labs 10%  
Test 25% (Feb. 19)  
Final Exam 55%

Submit all assignments directly to instructor (in-class or office by 5PM). Late assignments will be accepted up to 3 days after the due date with a penalty of 10% per day.

Failure to complete all laboratory requirements will result in a grade of N being awarded for the course.
The final grade obtained from the above marking scheme for the purpose of GPA calculation will be based on the percentage-to-grade point conversion table as listed in the current Undergraduate Calendar.

Assignment of E grade and supplemental examination for this course will be at the discretion of the Course Instructor. The rules for supplemental examinations can be found in the current Undergraduate Calendar:

http://web.uvic.ca/calendar/FACS/UnIn/UARe/Grad.html

**Course Objectives and Learning Outcomes**

Understand and apply principles of operation and design of modern electronic devices: (i) Equations describing device operation; (ii) Appropriate device models; (iii) Factors that determine device performance.

**Syllabus**


**Accommodation of Religious Observance**

http://web.uvic.ca/calendar/GI/GUPo.html

**Policy on Inclusivity and Diversity**

http://web.uvic.ca/calendar/GI/GUPo.html

**Standards of Professional Behaviour**

You are advised to read the Faculty of Engineering document, Standards for Professional Behaviour, at http://www.uvic.ca/engineering/assets/docs/professional-behaviour.pdf which contains important information regarding conduct in courses, labs, and in the general use of facilities.

Students who have issues with the conduct of the course should discuss them with the instructor first. If these discussions do not resolve the issue, students should feel free to contact the Chair of the Department by email or the Chair's Secretary to set up an appointment.

Cheating, plagiarism and other forms of academic fraud are taken very seriously by both the University and the Department. You should consult http://web.uvic.ca/calendar/FACS/UnIn/UARe/PoAcI.html for the UVic policy on academic integrity.

**Course Lecture Notes**

Unless otherwise noted, all course materials supplied to students in this course have been prepared by the instructor and are intended for use in this course only. These materials are NOT to be re-circulated digitally, whether by email or by uploading or copying to websites, which could in some cases constitute a breach of academic integrity as defined in the UVic Calendar.